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** CONTINUING DATA *****

None

** FOREIGN APPLICATIONS *****

None

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Alliance	IL	4	21	5
Verified and Acknowledged <i>H. Williams</i> Examiner's Signature	<i>HW</i> Initials			

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TITLE

Single-stage and multi-stage low power interconnect architectures

☐ All Fees☐ 1.16 Fees (Filing)